

CFA-1100 Series

CONTROLLER

Non-contact

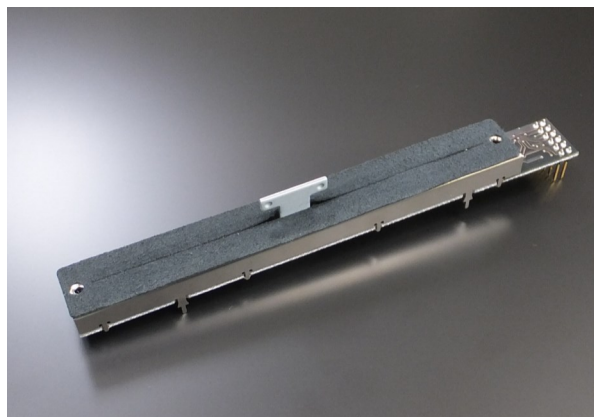
Long-term stability

Digital output

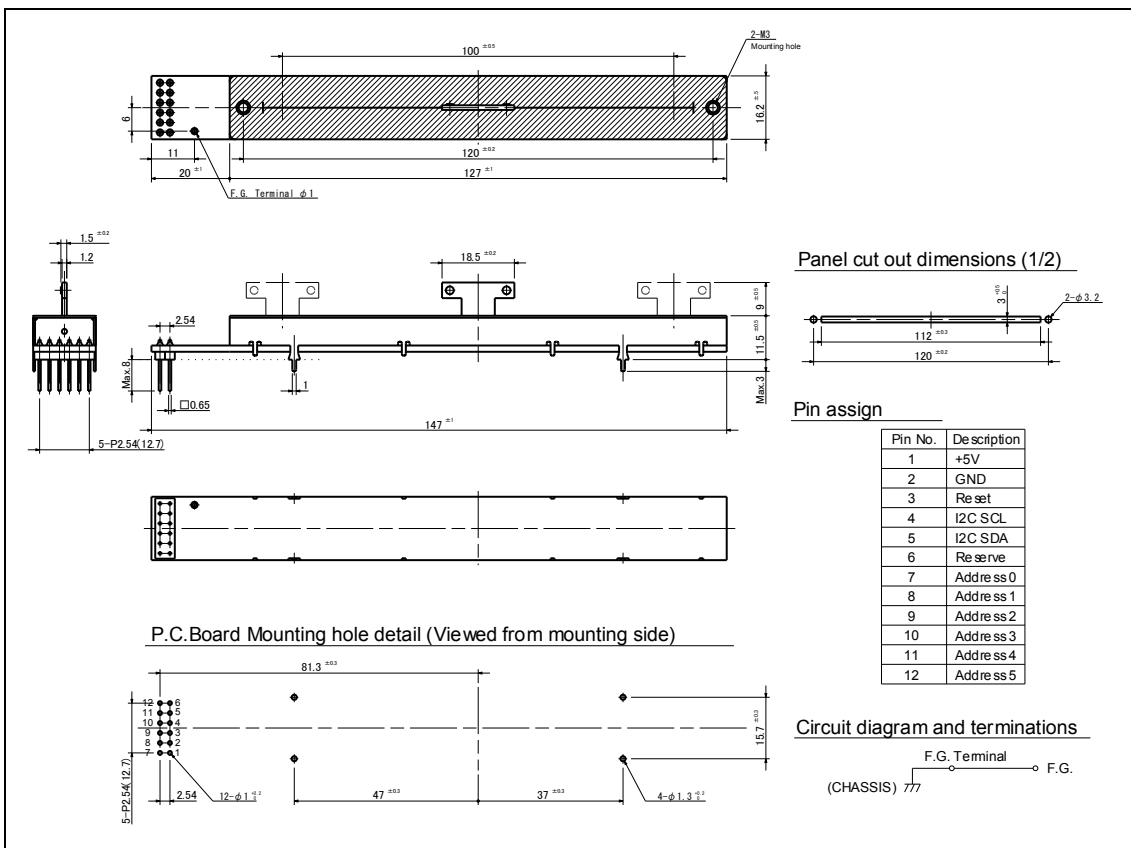
Facilitated in handling

High accuracy

Realized 10bit in 100mm stroke



Dimensions



Model number

CFA-110

1

- A

D

Product type
CFA-1100: 100mm

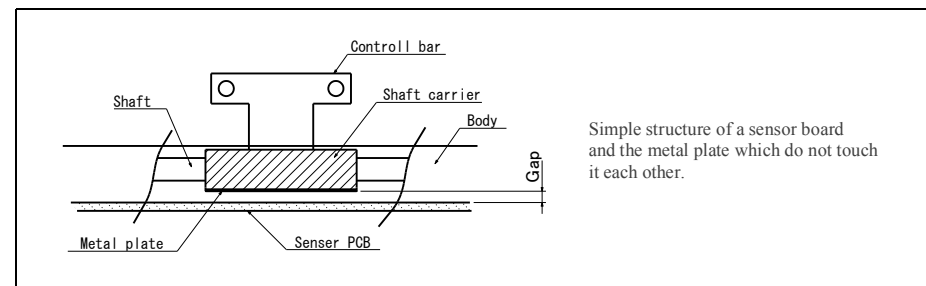
Torque
0: Normal torque
1: High torque *

Output
Blank: Incremental
A: Absolute

With Dust cover

* Only high torque type with CP-2 is applicable for the vertical use.

Structure



Electrical specifications

	CFA-110x	CFA-110x-A
Sensor system	Electrostatic capacitance type sensor	
Output value	Incremental type	Absolute type
Communication system	I ² C Slave	
Operating voltage	5V: ±0.25V	
Max. operating current	4mA Max.	
Resolution	10bit (0~1023)	
Output Law	1bit = 100mm/1024 (Linear)	
Bit error	±2bit	
Voltage proof	1 Min. at AC100V	
Insulation resistance	50Mohm or more at DC100V	

Mechanical specifications

	CFA-1100	CFA-1101
Stroke length	100mm±0.5mm	
Operating force	0~0.1N	0.1~0.3N
Strength of Nut-Attached	100Ncm	
Attached Parts	M3 screw (Length: Panel thickness + 3~4mm)	
Stopper strength	30N	
Push-pull strength	30N	

General specifications

	CFA-1100 Series
Temp.range	-10 to +70 deg C (Operating), -15 to +75 deg C (Storage)
Relative humidity	90%RH (No condensation)

Note

- * Non-waterproof.
- * Solder heat resistance: 350deg C max, 5sec max, 2 times. (Manual soldering only)
- * Do not give severe shocks.
- * Move to one end in Control-bar on the occasion of knob wearing, and can break into it slowly.

CFA-1100 Series

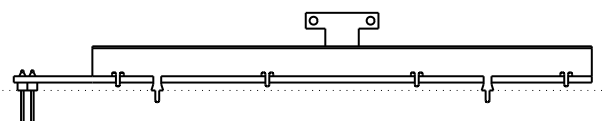
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I²C specifications

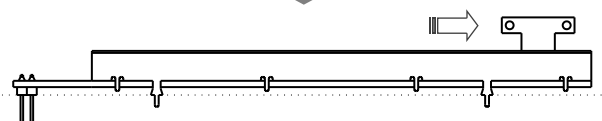
CFA-1100 Series																																																																																																																									
I ² C Clock	400kbps / 100kbps / 50kbps																																																																																																																								
Slave address	0~63																																																																																																																								
General call address	Not Supported																																																																																																																								
Transfer data	MSB First																																																																																																																								
Response time	1ms or less (I ² C Clock: 400kbps)																																																																																																																								
I ² C Communication behavior																																																																																																																									
	<table border="1"> <thead> <tr> <th>S</th> <th colspan="7">Slave Address</th> <th>R/W</th> <th>A</th> <th colspan="8">Data Byte</th> <th>A</th> <th>P</th> </tr> <tr> <th></th> <th>0</th> <th>SA5</th> <th>SA4</th> <th>SA3</th> <th>SA2</th> <th>SA1</th> <th>SA0</th> <th>1</th> <th>1</th> <th colspan="8"></th> <th>0</th> <th></th> <th></th> </tr> </thead> <tbody> <tr> <td>Master</td> <td>0</td> <td>SA5</td> <td>SA4</td> <td>SA3</td> <td>SA2</td> <td>SA1</td> <td>SA0</td> <td>1</td> <td>1</td> <td colspan="8"></td> <td>0</td> <td></td> <td></td> </tr> <tr> <td>CFA-1100</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>I²C Bus</td> <td>0</td> <td>SA5</td> <td>SA4</td> <td>SA3</td> <td>SA2</td> <td>SA1</td> <td>SA0</td> <td>1</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>P</td> </tr> </tbody> </table>	S	Slave Address							R/W	A	Data Byte								A	P		0	SA5	SA4	SA3	SA2	SA1	SA0	1	1									0			Master	0	SA5	SA4	SA3	SA2	SA1	SA0	1	1									0			CFA-1100	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	I ² C Bus	0	SA5	SA4	SA3	SA2	SA1	SA0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	P
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S = Start condition P = Stop condition A = Acknowledge SA = Slave address D = Output data bits																																																																																																																									

How to use (Incremental type)

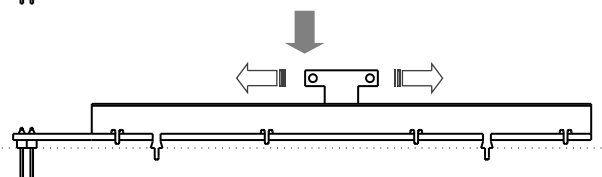
1. Power ON
Output data 0



2. Reset
Output data 0



3. Operation start
Output data 0~1023

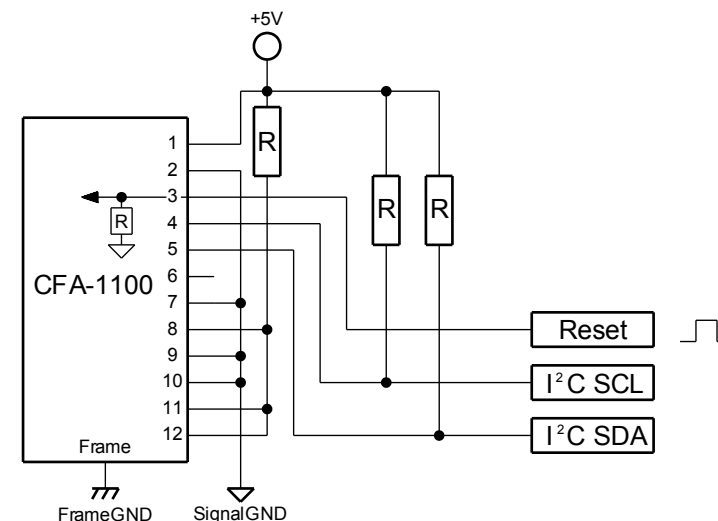


- At the time of power on, output data are 0, regardless of the position of the control bar.
- Resets works when the control bar is moved to the edge of the direction of the figure.
- After reset, position data in proportion to the movement of the control bar are output.

* In power-off, the most recent position data are not retained.

Circuit example

I²C Slave address 50 [decimal]



Connect the frameGND with the frame, otherwise with the F.G. through-hole.

Pin Assign

Pin No.	Description
1	Operating voltage DC+5V
2	Ground connection
3	Active high external reset with internal pull down
4	I ² C SCL
5	I ² C SDA
6	Reserve
7	I ² C Slave address bit0
8	I ² C Slave address bit1
9	I ² C Slave address bit2
10	I ² C Slave address bit3
11	I ² C Slave address bit4
12	I ² C Slave address bit5